



AMENDMENT UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
GROUP 2811
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q77597

Yasushi KINOSHITA

Appl. No.: 10/669,655

Group Art Unit: 2811

Confirmation No.: 5578

Examiner: Samuel A. GEBREMARIAM

Filed: September 25, 2003

For: SEMICONDUCTOR INTEGRATED CIRCUIT

AMENDMENT UNDER 37 C.F.R. § 1.116

MAIL STOP AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated April 6, 2006, please amend the above-identified application as follows on the accompanying pages.

TABLE OF CONTENTS

AMENDMENTS TO THE CLAIMS.....	2
REMARKS.....	5

Antiref
W/2CE
8-4-06
8-10-06

Do not enter